



PATENTS  
112056-0036  
P01-1433

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In Re The Application of: )  
Robert M. English et al. )

Serial No.: 10/035,603 )

Examiner: Kerveros, James C.

Filed: December 28, 2001 )

Art Unit: 2135

For: CORRECTING MULTIPLE )  
BLOCK DATA LOSS IN A )  
STORAGE ARRAY USING A )  
COMBINATION OF A SINGLE )  
DIAGONAL PARITY GROUP )  
AND MULTIPLE ROW PAR-  
ITY GROUPS )

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June 24, 2005

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Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

**PRELIMINARY AMENDMENT**

This Amendment is filed in connection with a Request for Continued Examination and a 2-month Extension of Time, and in response to the Final Office Action mailed on January 27, 2005. All objections and rejections are respectfully traversed.